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TSMC, Motorola and ASE are First to Launch RosettaNet Supply Chain Software

Hsinchu, Taiwan, July 11, 2001 - Taiwan Semiconductor Manufacturing Company Ltd. (NYSE: TSM), the world's leading pure-play foundry, is the first to successfully implement the RosettaNet PIP 3D8 standard between TSMC, Motorola, Inc., and Advanced Semiconductor Engineering, Inc. (NYSE: ASX). The RosettaNet Partner Interface Process (PIP) 3D8 standard defines an environment for automatically tracking work between a manufacturing facility and its customers or partners.

By connecting TSMC to a leading upstream integrated device manufacturer, and a leading downstream wafer probing, die assembly and final test service provider through the RosettaNet PIP interface, TSMC provides the first foundry-based supply chain management system to encompass all three major functions in the electronics manufacturing supply chain.

"The RosettaNet 3D8 standard allows us to raise the work-in progress (WIP) visibility of wafers and die. This shortens IC manufacturing lead-time and improves business planning for faster time-to-volume," said Dr. Quincy Lin, CIO and senior vice president of TSMC. "By linking the foundry's information with back-end wafer probing, die assembly and final test information, we're providing an environment that can significantly boost manufacturing efficiencies, setting new milestones and further improving our leadership position in the foundry industry."

"This early implementation of the RosettaNet 3D8 standard between three major partners in the IC manufacturing process proves the value of a standards-based supply chain management approach," said Ron Waters, director of worldwide wafer external technology, Motorola SPS. "The ease with which this system was put in place is a testament to the quality of work put forth by the members of RosettaNet's Semiconductor Manufacturing Board."

"Our RosettaNet collaboration with TSMC provides an integrated view of the packaging and test work-in-progress. As a result, our mutual customers have improved visibility of the complete semiconductor manufacturing, packaging and test activities," said Dr Sueming Shen, vice president of MIS at ASE. "This clearly benefits our customers by helping them match supply and demand and increasing their competitiveness. ASE also benefits through better capacity allocation and materials management, resulting in shorter lead times for our customers."

The RosettaNet 3D8 standard provides a format and protocol for WIP data exchange and was developed and approved by the RosettaNet Semiconductor Manufacturing Board. TSMC is the lead partner in the Manufacturing Focus Process Team under RosettaNet Semiconductor Board and is working on other RosettaNet PIP standards projects aimed at providing more efficient services and transparent logistics information to customers.

About RosettaNet

RosettaNet is a self-funded, non-profit consortium of more than 400 of the world's leading Electronic Components (EC), Information Technology (IT) and Semiconductor Manufacturing (SM) companies dedicated to creating, implementing and promoting open e-business standards. These standards forms a common e-business language, aligning processes between trading partners on a global basis. Additional information about RosettaNet can be found at <http://www.Rosettanet.org>

About Motorola

As the world's #1 producer of embedded processors, Motorola's Semiconductor Products Sector creates DigitalDNA? system-on-chip solutions for a connected world. Our strong focus on communications and networking enables customers to develop smarter, simpler, safer and synchronized products for the person, work team, home and automobile. Motorola's worldwide semiconductor sales were \$7.9 billion (USD) in 2000.

<http://www.motorola.com/semiconductors/>

Motorola, Inc. (NYSE:MOT) is a global leader in providing integrated communications and embedded electronic solutions. Sales in 2000 were \$37.6 billion. <http://www.motorola.com>

About ASE Inc.

ASE Inc. is one of the world's largest independent providers of semiconductor packaging services and, together with its subsidiary ASE Test Limited (NASDAQ: ASTSF), one of the world's largest independent providers of semiconductor testing services, including front-end engineering testing, wafer probing and final testing services. The Company's international customer base of more than 200 blue-chip customers includes such leading names as Advanced Micro Devices, Inc., Altera Corporation, Cirrus Logic Inc., Conexant Systems, Inc., LSI Logic Corporation, and Qualcomm Incorporated. With advanced process technology capabilities and a global presence spanning Taiwan, Korea, Hong Kong, Singapore, Malaysia and the United States, ASE Inc. has established a reputation for reliable, high quality products and services. For more information, visit the website, <http://www.aseglobal.com>

About TSMC

TSMC is the world's largest dedicated semiconductor foundry, providing the industry's leading process technology, library and IP options and other leading-edge foundry services. TSMC operates two six-inch wafer fabs and six eight-inch wafer fabs. The company also has substantial capacity commitments at two joint ventures fabs (Vanguard and SSMC) and

WaferTech. In 2000, TSMC produced the foundry industry's first 300mm customer wafers and began constructing two dedicated 300mm fabs. TSMC's corporate headquarters are in Hsin-Chu, Taiwan. More information about TSMC is available through the World Wide Web at <http://www.tsmc.com>

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